

Let's try to understand (part of) Iris

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The Paper

Iris: Monoids and Invariants as an Orthogonal
Basis for Concurrent Reasoning

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DISCLAIMER

I'm not an expert

Concurrency is about shared state

Situation	Shared state	Verify this
Shared memory	Memory	No secret overwrites, Counter only increases
Message-passing	Network	Protocol
Input/output	Filesystems, Humans, ...	Protocol

How to verify when there's concurrency?

“

”

Monoids and invariants are all you need

– Iris

Invariant: assertion about shared state

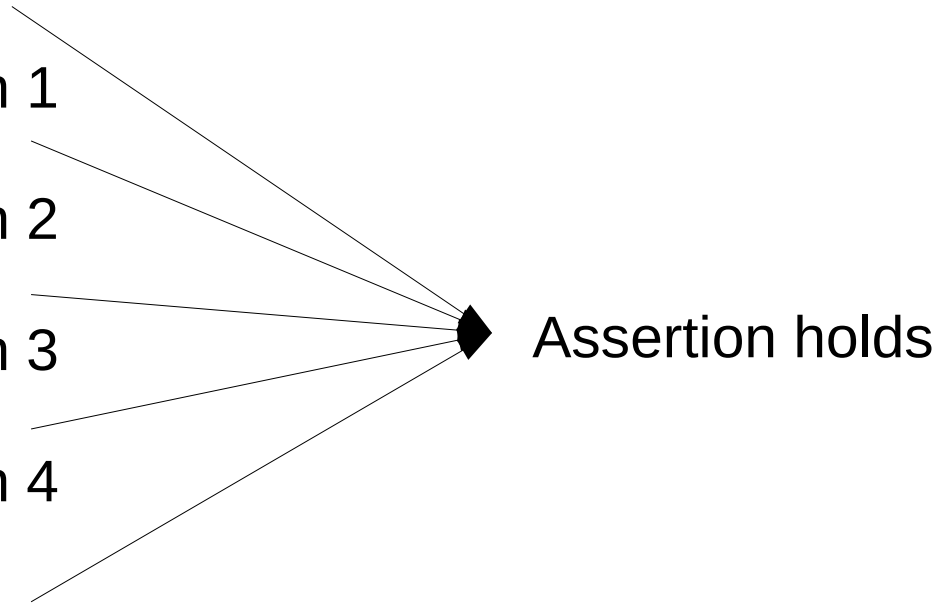
Thread 1:

Atomic operation 1

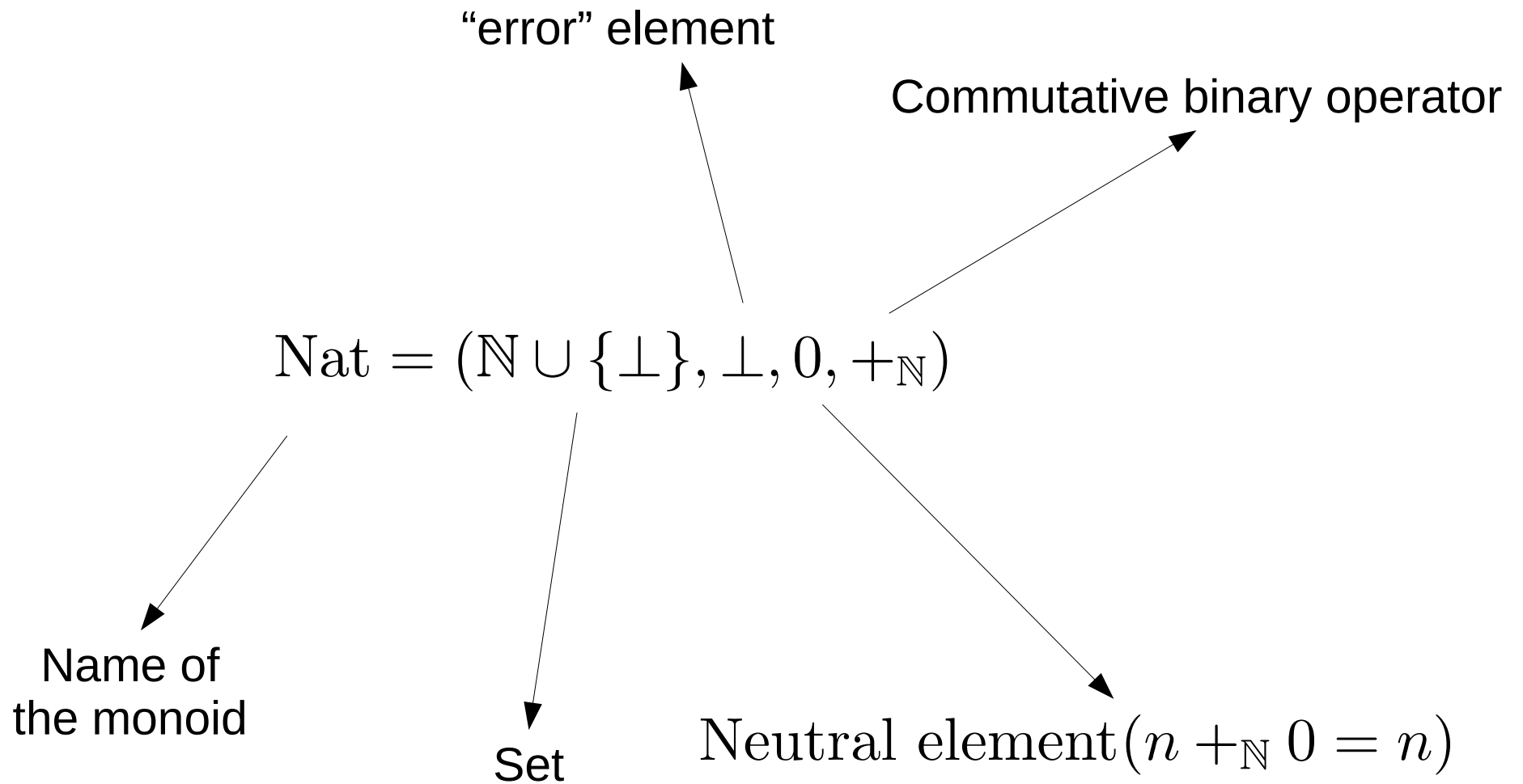
Atomic operation 2

Atomic operation 3

Atomic operation 4



(Iris-style) Monoid



“Case study”: Verification + concurrency + heap

Proglang:

`v = malloc()`

`v1 = !v2`

`v1 := v2`

`v1 = v2`

Attempt #1

Invariant, e.g.:

$$\exists h \in \text{Heaps}. [h]$$

→ **GLOBAL** physical state is h

$$M_h = (\text{Heaps} \cup \{\perp\}, \perp, \text{emptyheap}, \cdot_h)$$

$$\{1 \mapsto 2\} \cdot_h \{7 \mapsto 8\} = \{1 \mapsto 2, 7 \mapsto 8\}$$

$$\{1 \mapsto 2\} \cdot_h \perp = \perp$$

$$\{h : \text{Locations} \rightarrow \text{Values}\}$$

$$\{1 \mapsto 2\} \cdot_h \{1 \mapsto 2\} = \perp$$

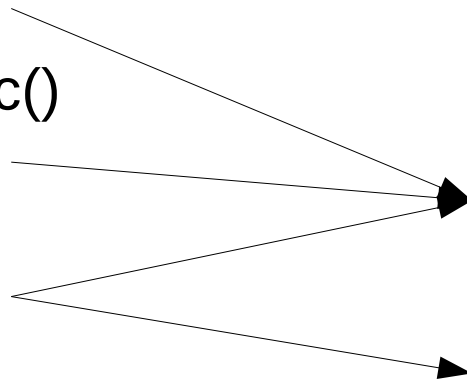
Thread 1:

`v1 = malloc()`

`v1 := 7`

$\exists h \in \text{Heaps}. [h]$

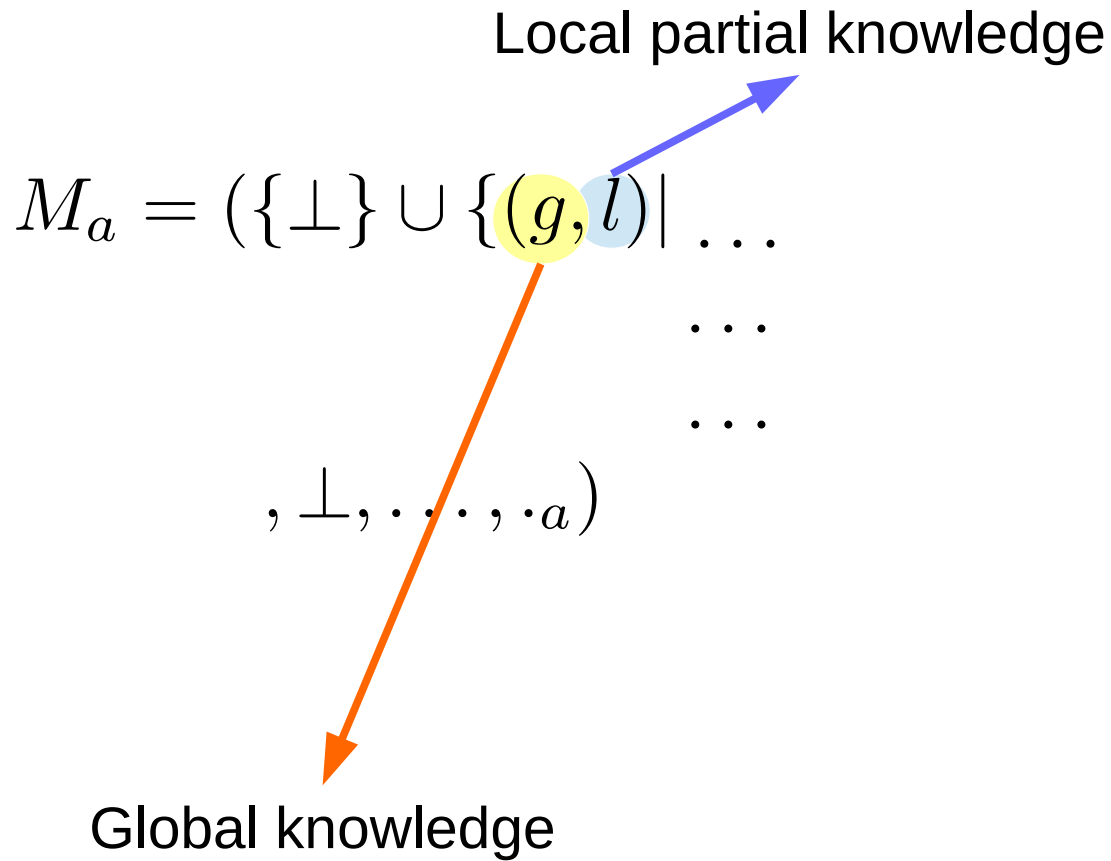
$\text{"}v1 \mapsto 7\text{" ?}$



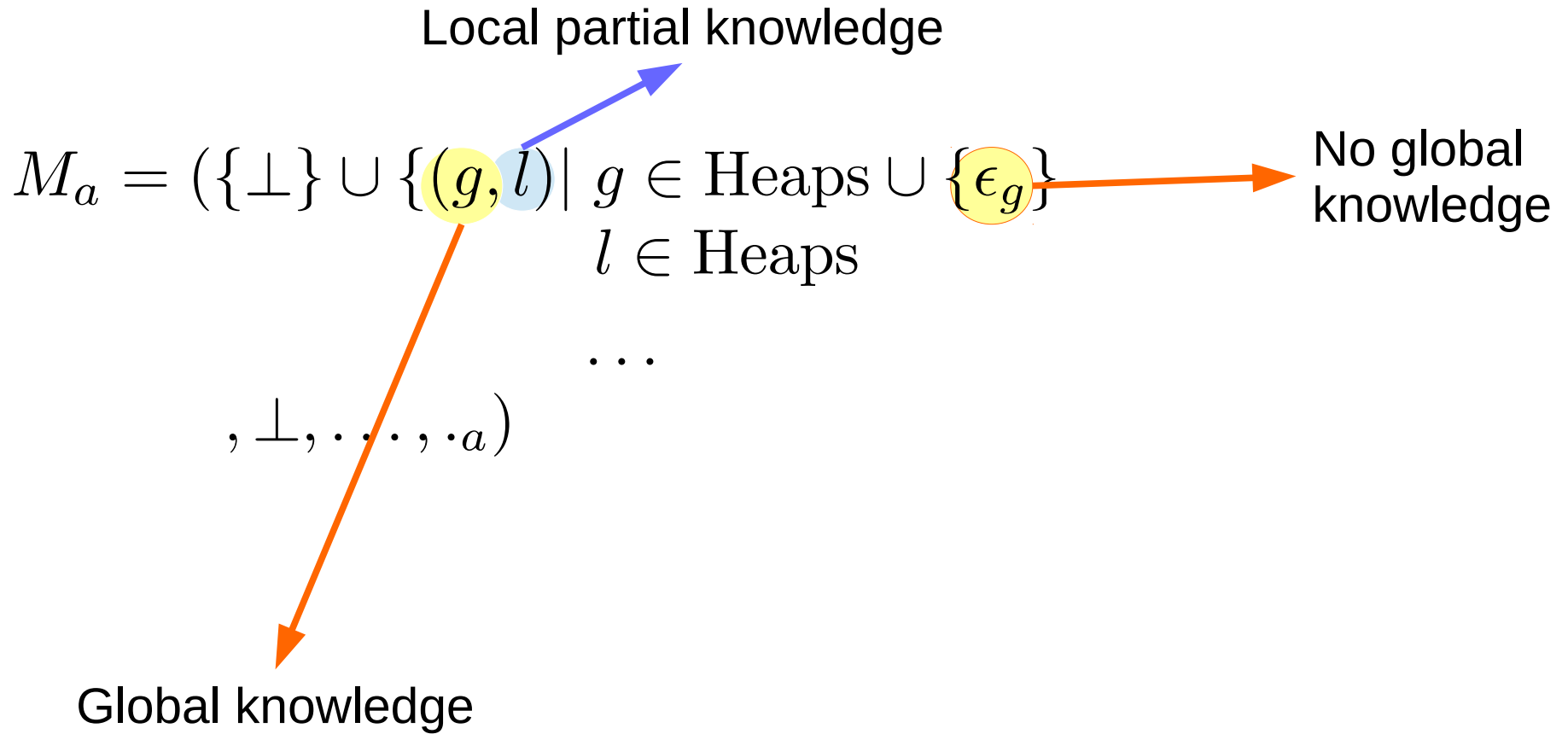
“partial knowledge” in monoid

$$M_a = \left(\begin{array}{c} \text{Set } |M_a| \\ , \perp, \dots, \cdot a \end{array} \right)$$

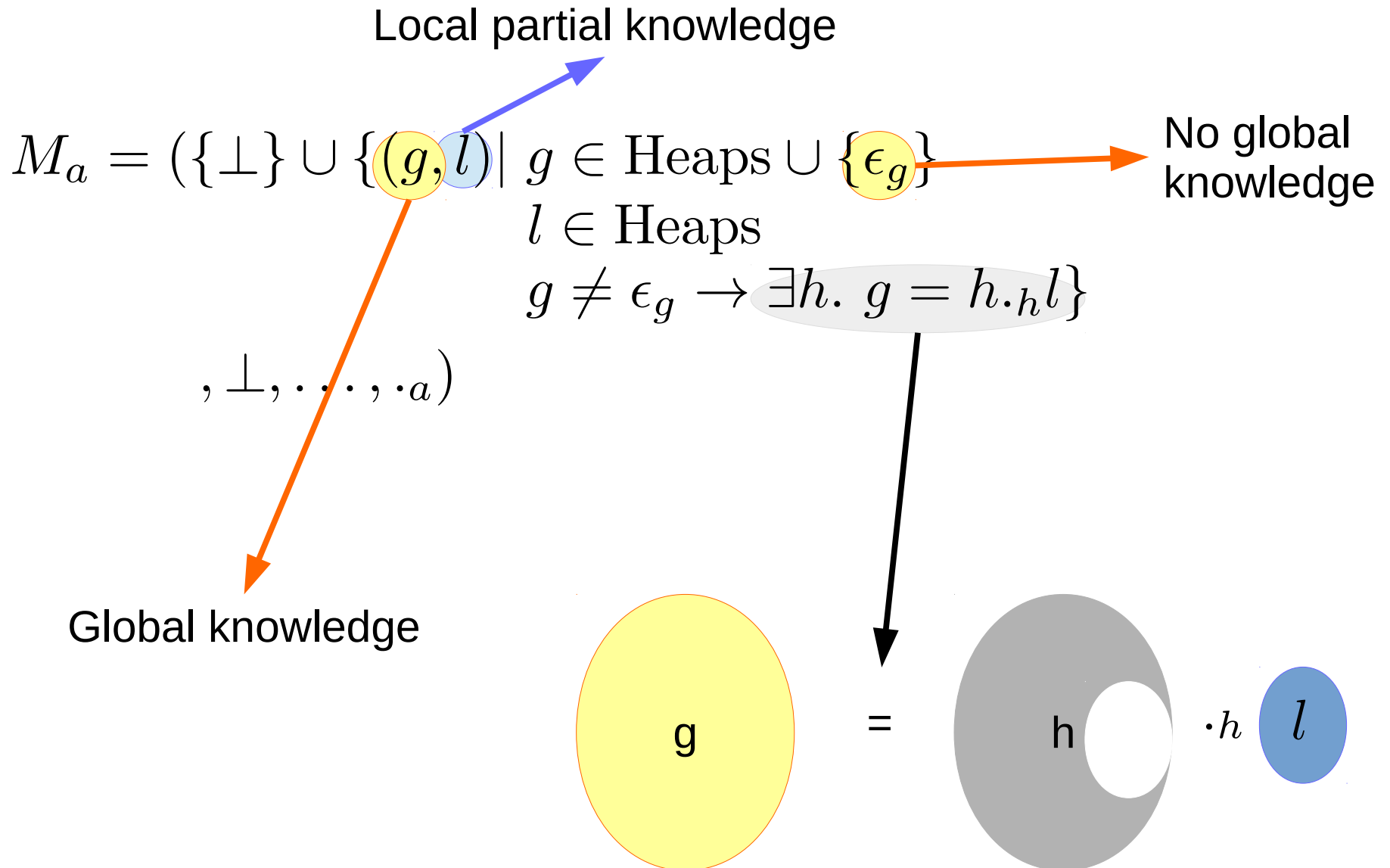
“partial knowledge” in monoid



“partial knowledge” in monoid



“partial knowledge” in monoid



“partial knowledge” in monoid

$$M_a = (\{\perp\} \cup \{(g, l) \mid \begin{array}{l} g \in \text{Heaps} \cup \{\epsilon_g\} \\ l \in \text{Heaps} \\ g \neq \epsilon_g \rightarrow \exists h. g = h \cdot_h l \end{array}\} \\ , \perp, \dots, \cdot a)$$

Exercise: what does this mean?

$(\epsilon_g, \text{emptyheap})$

$(\epsilon_g, (1 \mapsto 2, 102 \mapsto 7))$

$((1 \mapsto 2, 102 \mapsto 7), (1 \mapsto 2, 102 \mapsto 7))$

$(\text{emptyheap}, \text{emptyheap})$

$$M_a = (\{\perp\} \cup \{(g, l) \mid g \in \text{Heaps} \cup \{\epsilon_g\} \\ l \in \text{Heaps} \\ g \neq \epsilon_g \rightarrow \exists h. g = h.h l\})$$

, \perp , \dots , \cdot_a)

$$(\epsilon_g, l_1) \cdot_a (g, l_2) = (g, l_1.h l_2) \text{ if } (g, l_1.h l_2) \in |M_a| \setminus \{\perp\}$$

$$m_1 \cdot_a m_2 = \perp \text{ other cases}$$



Note: in paper composition
Is just pointwise
(so $(\backslash\text{eps}, l_1) \cdot (\backslash\text{eps } l_2)$ is not always $\backslash\text{bot}$)

Exercise: what is the neutral element?

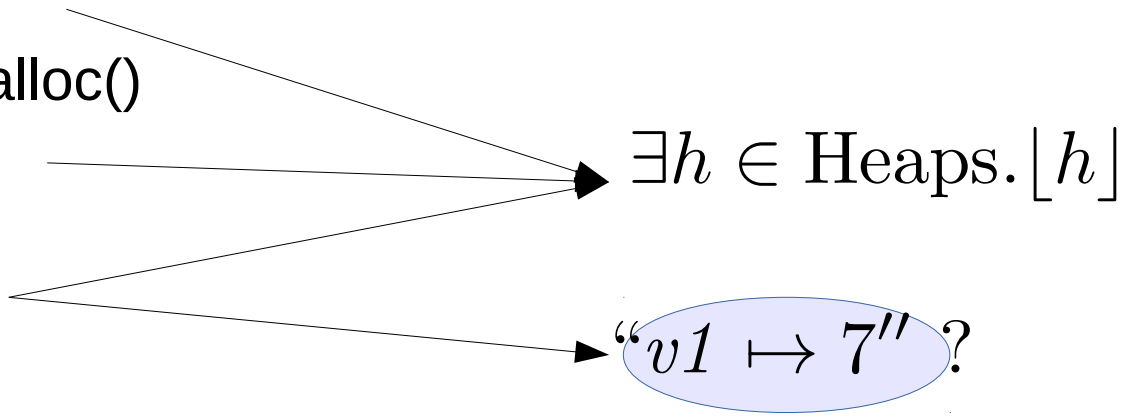
Thread 1:

`v1 = malloc()`

`v1 := 7`

$\exists h \in \text{Heaps}. [h]$

$"v1 \mapsto 7"?$



Thread 1:

$v1 = \text{malloc}()$

$v1 := 7$

$\exists h \in \text{Heaps}. \boxed{(h, \text{emptyheap})} * [h]$

~~$\exists h \in \text{Heaps}. [h]$~~

$\boxed{(\epsilon_g, \{v1 \mapsto 7\})}$

Link with physical?

Combined:

$\boxed{(\epsilon_g, \{v1 \mapsto 7\})} * \exists h \in \text{Heaps}. \boxed{(h, \text{emptyheap})} * [h]$

$= \exists h \in \text{Heaps}. \boxed{(\epsilon_g, \{v1 \mapsto 7\})._a(h, \text{emptyheap})} * [h]$

$= \exists h \in \text{Heaps}. \boxed{(h, \{v1 \mapsto 7\})} * [h]$

Know $v1 \mapsto 7$ in physical state!

Let's prove

$$\left\{ \left[(\epsilon_g, \{v1 \mapsto 0\}) \right] \right\} v1 := 7 \left\{ \left[(\epsilon_g, \{v1 \mapsto 7\}) \right] \right\} \{ \iota \}$$



Our invariant holds

Strategy

- Open invariant
- Combine thread's ghost state with invar's
 - $\boxed{m_1} * \boxed{m_2} = \boxed{m_1.m_2}$ \longrightarrow Know $a \mapsto _$ in physical state!
- Do physical update

 - $\{ \boxed{h[a \mapsto v_2]} \} \quad a := v_2 \quad \{ \boxed{h[a \mapsto v_2]} \}$
- Do ghost update
- Split thread's ghost state and invar's
- Close invariant

$$\left\{ \left[(\epsilon_g, \{v1 \mapsto 0\}) \right] \right\}$$

$$\left\{ \left[(\epsilon_g, \{v1 \mapsto 0\}) \right] * \exists h \in \text{Heaps}. \left[(h, \text{emptyheap}) \right] * [h] \right\}$$

$$\left\{ \exists h \in \text{Heaps}. \left[(\epsilon_g, \{v1 \mapsto 0\}) \right]._a (h, \text{emptyheap}) * [h] \right\}$$

$$\left\{ \exists h \in \text{Heaps}. \left[(h, \{v1 \mapsto 0\}) \right] * [h] \right\}$$

$$\left\{ \left[(h' [v1 \mapsto 0], \{v1 \mapsto 0\}) \right] * [h' [v1 \mapsto 0]] \right\}$$

frame

Phys. Upd

$$\{ [h' [v1 \mapsto 0]] \}$$

$v1 := 7$

$$\{ [h' [v1 \mapsto 7]] \}$$

$$\left\{ \left[(h' [v1 \mapsto 0], \{v1 \mapsto 0\}) \right] * [h' [v1 \mapsto 7]] \right\}$$

Need to update ghost state to close invar

$$\left\{ \left[(h' [v1 \mapsto 0], \{v1 \mapsto 0\}) * [h' [v1 \mapsto 7]] \right] \right\}$$

$$\left\{ \left[(h' [v1 \mapsto 7], \{v1 \mapsto 7\}) * [h' [v1 \mapsto 7]] \right] \right\}$$

$$\left\{ \left[(\epsilon_g, \{v1 \mapsto 7\}) * \exists h \in \text{Heaps}. \left[(h, \text{emptyheap}) * [h] \right] \right] \right\}$$

$$\left\{ \left[(\epsilon_g, \{v1 \mapsto 7\}) \right] \right\}$$

???

Allowed if “does not harm other threads”

“Does not harm other threads”

$(\epsilon_g, \text{emptyheap}) \rightsquigarrow \{(\epsilon_g, \{72 \mapsto 0\})\}$?

No: other thread might have e.g. $(\epsilon_g, \{72 \mapsto 123\})$

$(\{72 \mapsto 12, 1 \mapsto 3\}, \{72 \mapsto 12\})$
 $\rightsquigarrow \{(\{72 \mapsto 0, 1 \mapsto 3\}, \{72 \mapsto 0\})\}$?

Yes: cell update


$$a \rightsquigarrow \{b\} \iff \forall f : a.f \neq \perp \Rightarrow b.f \neq \perp$$

Increase-only counter

$$M_c = (\{\perp\} \cup \{(g, l) \mid \begin{array}{l} g \in \mathbb{N} \cup \{\epsilon_c\} \\ l \in \mathbb{N} \\ g \neq \epsilon_g \rightarrow l \leq g \end{array}\}, \perp, \dots, \cdot_c)$$

$$(\epsilon_c, l_1) \cdot_a (g, l_2) = (g, \min(l_1, l_2)) \text{ if } (g, \min(l_1, l_2)) \in |M_c| \setminus \{\perp\}$$
$$m_1 \cdot_c m_2 = \perp \text{ other cases}$$

Wrapping up

- Monoids
- Physical assertion
- Ghost assertion
- Invariants
- 

Teaser Episode 3

- Can I model I/O in Iris? (Willem)
- Logical Atomicity (Amin)